

Basic Input/Output System Updating Method

BACKGROUND OF THE INVENTION

1) FIELD OF THE INVENTION

5 The invention herein relates to basic input/output system (BIOS) maintenance in electronic devices such as notebook computers, specifically a basic input/output system updating method.

2) DESCRIPTION OF THE PRIOR ART

10 A basic input/output system (BIOS) is typically a section of program code in the read-only memory (ROM) of a mainboard circuit and consists of numerous input/output interface basic control code sequences that are executed as a self-diagnostic operation after a computer power supply is switched on and, furthermore, involves the reading of data (such as hard disk drive size, the presence of an optical drive, and system time, and
15 other similar data) programmed into a complimentary metal oxide semiconductor (CMOS). As such, if the said BIOS is destroyed, the computer system cannot be started up and initialized. However, notebook computers now available on the market all have seamless BIOS protection measures such that when the said BIOS is corrupted and destroyed by virus intrusion or lost due to user-attributed BIOS reconfiguration failure,
20 the said computer system cannot be started up and initialized, which requires repair and results in considerable user inconvenience.

SUMMARY OF THE INVENTION

25 An objective of the invention herein is to provide a basic input/output system updating method that divides a memory in an electronic device into a system program

area and a boot program area, wherein a basic input/output system (BIOS) program is stored in the said system program area and an initialization sequence is stored in the boot program area. When the initialization sequence in the said system program area is destroyed and the BIOS program is lost, user is able to toggle a switch to a closed position to connect the said microprocessor to the said boot program area and thereby enable the connection of the said microprocessor to the boot program area and read the initialization sequence stored in the boot program area. As such, the said microprocessor, during the initialization sequence, activates a memory components connected with the said electronic device, reads a BIOS program stored in the said memory components, and records it into the system program area, thereby circumventing the failure of the said electronic device to initialize because the BIOS program was lost from the said system program area.

Another objective of the invention herein is to provide a basic input/output system updating method that divides the memory in an electronic device into a system program area and a boot program area, wherein the BIOS program is stored in the said system program area and the initialization sequence is stored in the boot program area such that when the said system program area is destroyed and the BIOS program is lost, the user toggles a switch to the closed position to connect the said microprocessor to the said boot program area to thereby enable the connection of the said microprocessor to the boot program area and its reading of the initialization sequence in the boot program area. As such, during the initialization sequence, the said microprocessor, during the initialization sequence, activates a memory components connected with the said electronic device, reads a BIOS program stored in the said memory components, and records it into the system program area, thereby circumventing the failure of the said electronic device to initialize because the BIOS program was lost from the said system program area.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a schematic diagram of the invention herein.

Figure 2 is a flow chart of the invention herein.

DETAILED DESCRIPTION OF THE INVENTION

The invention herein is a basic input/output system updating method, referring to FIG. 1, that divides a memory 11 in an electronic device 1 (such as a notebook computer) into a system program area 111 and a boot program area 112, wherein the said basic input/out system (BIOS) program is stored in the said system program area 111 such that, when a microprocessor 13 of the said electronic device 1 reads the said program, it executes the system initialization sequence of the said electronic device 1. While the said system program area 111 be destroyed and the BIOS program is lost, the user toggles a switch SW1 to the ON position to connect the said microprocessor 13 to the said boot program area 112 and thereby enable the reading of an initialization sequence stored in the boot program area 112.

In the invention herein, the said microprocessor 13, during the said initialization sequence, activates the hard disk drive connected, reads a BIOS program stored in the hard disk drive, and records the said BIOS read from said hard disk drive into the said system program area 111.

Referring to FIG. 1, the memory 11 of the invention herein is a flash read-only memory (ROM) and the said switch SW1 is a manually operated shunting component that enables the user to toggle the said switch SW1 into a closed state (ON) or an open state (OFF); when the user switches on the power supply of the said electronic device 1, the said microprocessor 13 issues an address signal (such as FFFF0) via a decoder which is sent after decoding to the said system program area and boot program area 111 and 112,

thereby enabling the said microprocessor 13 to respectively assign the starting position of the said system program area and boot program area 111 and 112 such that when the user toggles the said switch SW1 to the ON state and brings the said microprocessor 13 into a connection with the said boot program area 112, the said microprocessor 13 reads from the said boot program area 112 and writes into the said boot program area 112; or, when the user toggles the said switch SW1 to the OFF state and thereby establishes a connection between the said microprocessor 13 and the said system program area 111, the said microprocessor 13 reads from the said system program area 111 and writes into the said system program area 111.

In the most preferred embodiment of the invention herein, referring to FIG. 1, the said microprocessor 13, during the said initialization sequence, activates the integrated drive electronics (IDE) interface of the said electronic device 1, reads a BIOS program stored in the IDE-interface connected to the hard disk drive, and records the said IDE BIOS into the said system program area 111.

In the said embodiment, referring to FIG. 1, the memory 11 of the invention herein is a flash ROM and the said switch SW1 is a manually operated component that enables the user to toggle the said switch SW1 into a closed state (ON) or an open state (OFF); when the user switches on the power supply of the said electronic device 1, the said microprocessor 13 issues an address signal (such as FFFF0) via a decoder which, following decoding, is sent to the said system program area and boot program area 111 and 112, thereby enabling the said microprocessor 13 to respectively assign the starting position of the said system program area and boot program area 111 and 112 such that when the user toggles the said switch SW1 to the ON state and brings the said microprocessor 13 into a connection with the said boot program area 112, the said microprocessor 13 reads from the said boot program area 112 and writes into the said

boot program area 112; or, when the user toggles the said switch SW1 to the OFF state and thereby establishes a connection between the said microprocessor 13 and the said system program area 111, the said the said microprocessor 13 reads from the said system program area 111 and writes into the said system program area 111.

5 In another most preferred embodiment of the invention herein, referring to FIG. 1, the said microprocessor 13 links with a local area network (LAN) during the said initialization sequence, downloads a BIOS program from the said LAN, and records the said BIOS downloaded from the said LAN into the said system program area 111.

10 In the said embodiment, referring to FIG. 1, the memory 11 of the invention herein is flash ROM and the said switch SW1 is a manually operated component that enables the user to toggle the said switch SW1 into a closed state (ON) or an open state (OFF); when the user switches on the power supply of the said electronic device 1, the said microprocessor 13 issues an address signal (such as FFFF0) via a decoder which, following decoding, is sent to the said system program area and boot program area 111
15 and 112, thereby enabling the said microprocessor 13 to respectively assign the starting position of the said system program area and boot program area 111 and 112 such that when the user toggles the said switch SW1 to the ON state and brings the said microprocessor 13 into a connection with the said boot program area 112, the said microprocessor 13 reads from the said boot program area 112 and writes into the said
20 boot program area 112; or, when the user toggles the said switch SW1 to the OFF state and thereby establishes a connection between the said microprocessor 13 and the said system program area 111, the said the said microprocessor 13 reads from the said system program area 111 and writes into the said system program area 111; in addition, the computer manufacturer can down-load the said BIOS through the said LAN.

25 Referring to FIG. 1 and FIG. 2, when the user switches on the said electronic

device 1, the said microprocessor 13 executes the following procedures:

At step 101, a determination is made as to whether the said switch SW1 is in the ON state. If the result is affirmative, the next procedure is executed; if the result is negative, then the BIOS program in the system program area 111 of the said memory 11 is read to execute the normal initialization routine.

At step 102, the boot program area 112 in the said memory 11 is read to preset the initialization sequence.

At step 103, the said initialization sequence activates the hard disk drive operation of the said electronic device 1.

At step 104, a determination is made as to whether a BIOS program is stored in the said hard disk drive. If the result is affirmative, the said BIOS program is read and recorded in the system program area 111 of the said memory 11, which concludes the procedure; otherwise, the sequence proceeds to the next step.

At step 105, the said initialization sequence activates the IDE interface operation of the said electronic device 1.

At step 106, a determination is made as to whether a BIOS program is present in the IDE interface connected to the hard disk drive. If the result is affirmative, the said BIOS program is read and recorded in the system program area 111 of the said memory 11, which concludes the procedure; otherwise, the sequence proceeds to the next step.

At step 107, the said initialization sequence activates the LAN.

At step 108, linked to the LAN, the BIOS program of the said LAN is downloaded and recorded in the system program area 111 of the said memory 11, which concludes the sequence.

As such, when the BIOS program is lost from the system program area 111 in the memory 11 of the said electronic device 1, the said microprocessor 13 can read the said

BIOS program in the said system program area 111 to execute the normal initialization sequence; when the BIOS program of the said system program area 111 is destroyed by a computer virus or a user-induced BIOS reconfiguration failure such that the BIOS program is lost from the said system program area 111, the said microprocessor 13 is
5 capable of reading the initialization sequence from the said boot program area 112 to activate the hard disk drive, the IDE interface, and the LAN as well as record the BIOS program previously stored therein to the said system program area 111, thereby circumventing the failure of initializing the said electronic device 1 because the BIOS program was lost from the said system program area 111.

10 While the invention has been described by means of specific embodiments, numerous modifications and variations could be made thereto by those skilled in the art without departing from the scope and spirit of the invention set forth in the claims.